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HBCC Initialization Parameters (PWM Operations)

Upon startup of the AVT-715, AVT-716, AVT-921, and AVT-931 interface units in PWM mode, the HBCC device is initialized and a local loopback test is conducted. If the local loopback test passes successfully, the HBCC device operational parameters are then set to default parameters and the HBCC network drivers are enabled.

In the event the HBCC local loopback test fails for any reason an error code is sent to the control computer and the HBCC network drivers are disabled.

Most HBCC registers and RAM locations are set to default values during PWM mode initialization. The following is a list of these registers and RAM locations along a brief explanation of the initialization status and the value written to each location. The user should consult the HBCC User's Guide for detailed information on each of the registers, RAM, and individual bit definitions.

<u>Name</u>	<u>Address</u>	<u>Default Value</u>	<u>Description</u>
UIMR User Interrupt Mask Register	Register \$07	\$FF	All user interrupt disabled
RCR Receive Control Register	Register \$01	\$04	Receive OK interrupt mask Receive error interrupt mask Receiver overrun interrupt mask Unable to acknowledge interrupt mask Network fault interrupt mask
HCR HBCC Control Register	Register \$02	\$72	Enable network driver A Enable network driver B 41.6 kbps never sleep
TCR Transmit Control Register	Register \$00	\$0x	Transmit OK interrupt mask Transmit error interrupt mask Critical transmit error interrupt mask
NAR Node Address Register	Register \$10	\$F1	OBD-II diagnostic tool address

<u>Name</u>	<u>Address</u>	<u>Default Value</u>	<u>Description</u>
FMLT Function Message Lookup Table	RAM \$10	\$5A	OBD-II diagnostic message.
	RAM \$11	\$5B	OBD-II diagnostic message.
	RAM \$12	\$6A	OBD-II diagnostic message.
	RAM \$13	\$6B	OBD-II diagnostic message.
FRMLT Function Read Message Lookup Table	RAM \$14	\$04	
	RAM \$15	\$06	
FRDR1 Function Read Data Register #1	Register \$08	\$B2	
FRDR2 Function Read Data Register #2	Register \$09	\$B4	
FRDR3 Function Read Data Register #3	Register \$0A	\$B6	
FRMLTP Function Read Message Lookup Table Pointer	Register \$11	\$14	
SUR Start of User's RAM	Register \$12	\$16	